Docket No.: 57454-965 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Isao NOJIRI, et al. : Confirmation Number:

Serial No.: Group Art Unit:

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Filed: August 27, 2003 : Examiner:

For: SEMICONDUCTOR DEVICE WITH PROTECTION CIRCUIT PROTECTING

INTERNAL CIRCUIT FROM STATIC ELECTRICITY

## INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of the Japanese Patent Laying-Open No. 11-119257, is discussed in the present specification, is discussed in the present specification.

Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including

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Respectfully submitted,

MCDERMOTT, WILL & EMERY

Stephen A. Becker Registration No. 26,527

600 13<sup>th</sup> Street, N.W. Washington, DC 20005-3096 (202) 756-8000 SAB:gav Facsimile: (202) 756-8087

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		JP 11-119257A		04/30/1999	FUJITSI LTD.			JAPAN (w/	140	
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